

1c903 U.S. PTO
09/880749

09/12/01

714 724	Class	Subclass	ISSUE CLASSIFICATION
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PATENT NUMBER

U.S. UTILITY Patent Application

SA SCANNED <i>Eda</i> O.A. <i>for</i>	O.I.P.E.	PATENT DATE
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APPLICATION NO. 09/880749	CONT/PRIOR D	CLASS <i>714</i>	SUBCLASS <i>724</i>	ART UNIT <i>2838</i>	EXAMINER <i>J. KERVEROS</i>
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APPLICANTS
TITLE

Andrew Draper

2133

JTAG mirroring circuitry and methods

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
<i>714</i>	<i>724</i>	<i>714</i>	<i>726</i>					
INTERNATIONAL CLASSIFICATION								
<i>G01R</i>	<i>31/28</i>							

☐ Continued on Issue Slip Inside File Jacket

☐ TERMINAL
DISCLAIMER

DRAWINGS

CLAIMS ALLOWED

<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	Sheets Drwg. <i>10</i> Figs. Drwg. <i>16</i> Print Fig. <i>6</i>	Total Claims <i>29</i> Print Claim for O.G. <i>1</i>
	NOTICE OF ALLOWANCE MAILED ISSUE FEE Amount Due _____ Date Paid _____	
	KERVEROS, JAMES (Assistant Examiner) <i>4/26/04</i> (Date)	ISSUE BATCH NUMBER

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